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~ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####To describe the activities system
engineering, software and test organizations perform during product development
Activities described according to the requirements analysis, design,
code/integration, and formal test phasesActivity review and approval process
Intermediate Products#

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~ÿÿ#!##ÿÿ#####ÿ¥#ÿÿ#d#####d#####Requirements Analysis Phase##

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~ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####ÐPurpose - Analyze product requirements to determine feasibility, implementation approach, CONOPS, and requirements interpretation. Baseline the product.ProductsFRD Part 1 - Requirements StatementFeasibility Analysis - SOR implementation and cost basis RAM - Specification level description of ðsystem changeð and traceability matrix FRD Part 2 - Specification inputs, draft CONOPS, performance criteriaSRR Development PlanCost Plan and Earned Value Management#

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Engineering Responsibilities#

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[]ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####ùFRD Part 1 - review with and receive
IPT and sponsor approvalFeasibility Analysis - review and approvalRAM - review
and approvalCONOPS/Performance Goals - review and approvalSRR - Preparation of
materials to satisfy entry criteria and key items##

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Responsibilities##

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~ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####Feasibility Analysis Generation -
steps to implement the requirement. Staffing plan. SLOC estimate. SOR schedule
to TAMPS process.Revised form for 6.2.RAM review and comment.CONOPS/Performance
Criteria - Generation of user perspective of SOR. Generation of performance
goals.Improved CONOPS process improvement and TEMP reviewSOR SRR Materials
preparation - description of SOR implementation approach according to RAM.
Provide indication of SORs that will affect existing interfaces/IDD and extent
of affect#

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~ÿÿ#!##ÿÿ#####ÿ¥#ÿÿ#d#####d#####8Requirements Phase - Test
Organization Responsibilities#

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~ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####~RAM Update - Determination of
affected parts of system, generation of specification statements, and trace
matrixSpecification Statements - draft generation of SOR specification
paragraphsCONOPS/Performance Goals - review of inputs FRD Part 2 Generation -
Combination of spec. statements, CONOPS, and performance goalsIf CONOPS
multipage then insert or reference a separate doc.#

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~ÿÿ#!##ÿÿ#####ÿ¥#ÿÿ#d#####d#####&Requirements Phase - Plan
Development#

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deliverables, and schedule for the productDescription of the development process
Cost Plan - Costs to SOR level with planned spending by month. Basis for CSSR
and earned value management.Cost plan for all components of product.
Development, test, documentation, engineering, task management, ODCs, travel,
etc. - Complete cost product cost.##

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~ÿÿ#!##ÿÿ#####ÿ#ÿÿ#d#####d#####High Level Design Phase #

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Nÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####*Purpose - Determine how the components of the product work (1) with one another, (2) with surrounding HW and SW, (3) and internally. Design identifies product components, functional mission of each component, and at a òhigh leveló the internal design of each component.ProductsFRD Part 1/2 Update; Initial Part 3TAMPS Specification - Product updateTAMPS IRS - Product updateTAMPS IDD - Preliminary ICNs (function identification)FUIWG - HMI and CONOPS concurrence; STR listHMI IS CRITICAL Preliminary ITP - Plan for conducting the test processPDR#

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~ÿÿ#!##ÿÿ#####ÿÿ#ÿÿ#d#####d#####=High Level Design Phase - System
Engineering Responsibilities##

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vÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####`TAMPS Specification - Production of
spec.TAMPS IRS - Production and approval after reviewPeer Review Materials
Review (To be added for 6.3, for 6.2 a TIM prior to formal PDR at which all SORs
will be addressed)Requirements Trace & Assumptions - Review and approvalCONOPS
Update - Review and approvalDraft User Interface - Review and approvalPrelim.
IDD - draft ICN reviewDraft SOF - reviewTest Cases - reviewPreliminary STR List
- List of STRs for correctionFUIWG - Mtg. prep, STR list prep., CONOPS/MMI
reviewPreliminary ITP - reviewPDR - Preparation of materials to satisfy entry
and key items#

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Responsibilities##

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Öÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####~IRS inputs - Redlines/additions to existing IRS; review/comment to product IRSRequirements Trace & Assumptions - Trace within design, SOF is and code entities, specific interpretations, derived reqs., associated requirements. Part of Design Notebook.Trace to be to a lower level for 6.2; trace to tri_graph will not be validCONOPS Update - Changes based upon high level design - Baseline CONOPS at PDRData Flow Diagram - SOR data source, operations, changes to TAMPs data base. Part of Design Notebook.Control Flow Diagram - SOR control mechanisms and primary logic paths. Part of Design Notebook.Draft User Interface - Proposed HMI#

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Responsibilities##

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6ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####FUIWG - Presentation material prep.
for CONOPS & HMIPrelim. IDD - Function purpose, affected MPMs, draft ICNDraft
SOF - description of implementationPeer Review Materials prep. (To be added for
6.3, for 6.2 a TIM prior to formal PDR at which all SORs will be addressed)
Requirements Trace & Assumptions - Review and approvalCONOPS Update - Review and
baselineDraft User Interface - Review and approvalPrelim. IDD - draft function
definition reviewDraft SOF - reviewTest Cases - reviewPreliminary ITP - review#

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Organization Responsibilities##

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that requirements are testable and reflect RAMIRS - Review/comment to product
IRSRequirements Trace & Assumptions - Review CONOPS Update - ReviewDraft User
Interface - ReviewDraft SOF - review and incorporation into FRD Part 3Test Cases
- Cases for each SOR indicating test and approvalPreliminary ITP - development
of plan for Core and MPM test with CorePDR - description of the test process##

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~ÿÿ#!##ÿÿ#####ÿ×#ÿÿ#d#####d#####Detailed Design Phase #

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Engineering Responsibilities##

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vÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####SSTR PEAs - review and categorize
list with schedule and cost plan for PMA-233 approvalIDD - document production,
ICWG, and IDD update/approvalPeer Review - Lead review, prepare action report,
and approve design.SDD - Prepare SDDTest Cases - reviewTest Procedures - review
CDR - Preparation of materials to satisfy entry and key items##

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Responsibilities#

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Responsibilities#

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~ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####tUser Interface Update - update
incorporation into design notebookCONOPS Update - update incorporation into
design notebookSTR PEAs - analysis with estimated hours to correct STRs on list
SOF Update - update incorporation into design notebookData Flow Update - reflect
detail design and incorporation into design notebookControl Flow Update -
reflect detail design and incorporation into design notebook.Database
Schema/Query - changes to database for SORs and changes to system queries.
Incorporation into design notebook.Algorithms - description of any developed or
integrated for SOR. Incorporated into design notebook.#

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Organization Responsibilities#

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Unit test to unit test plan. Integrate and function test according to function
test plan. Code and test STRs. Integrate MPMs and external interfaces. Document
and correct DTRs. Execute system test on development HW.ProductsUnit Test Report
- results Function Test Report - resultsSystem Test Report - resultsDTRs -
Defects found and resolution statusPEAs - Submission of software for
incorporation in CM baselineTest Procedures - Completed procedures used to
achieve soft freezeSoft Freeze Test Report - Status of software to enter formal
testCM Build - Product software for formal test#

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Nÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####µApproval of SOR for Core and Soft
FreezeIntegration - Lead integration effort including DTR assignments and
prioritizationEXPECT all DTRs to be correctedTest Procedures - review ##

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~ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z##### Code - SOR code to approved
designSTR Code - code to approved PEASOR Unit Test - test and test report
according to planSOR Function Test - test and test report according to planSign
off - developer management sign-off of SOR PRIOR to demonstration to test!SOF
Update - update incorporation into design notebookSOR Integration - integrate
code in accordance with integration/functional test plan and status reporting
System Integration, MPM & External Interface Integration - Lab. and off-site
integration as needed.##

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- Lab. and off-site integration as needed.SOR Demonstration - review of unit
test report, functional test report, and demonstration of functional test from
developer.Core Freeze Approval - provide recommendation whether SW acceptable
for Core freezeSTR Test- Validation of STR fixes on development hardware (and
target system as feasible)SOR Test - Exercise procedures on development HW.
Update procedures to actual implementation. Write and track DTRs.Sort Freeze
Approval - provide recommendation whether SW acceptable for Soft freeze#

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#ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####Purpose - Perform test on CM baseline on target HW to identify any show-stopper STRs. Correct those STRs and retest. Prepare hard freeze baseline and conduct TRR. Perform regression test. Prepare test reports and OPNOTES. Support OTRR process and DT/OT. Provide master tapes for NRaD production.ProductsTarget System Test Report - results to certify ready for hard freeze.TRR - approval to validate and certify TAMPs product.Formal Test Report - results to certify ready to enter the OTRR process and DT/OTOTRR - approval to begin DT/OTOPNOTES - user addendumRR - approval for product release to FleetGold Tape/CD - Product to NRaD for Fleet reproduction#

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Fÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####Integration - Lead integration effort
for any identified critical changesTRR - preparation of materials to support
meetingOTRR - preparation of materials to support meetingRR - preparation of
materials to support meetingGold Tape/CD - production of Fleet release master
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formal test fixes needed for release approval#

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~ÿÿ####ÿÿ####Uÿ×#ÿÿ#U#####Z#####[]Target Test - execution of system
test procedures to validate product changes on all HW configurations and test of
selected portions of other system functionsTarget System Test Report - results
to support TRRRRegression Test - Execution of full system test procedures for all
HW configurationsFormal Test Report - results to support OTRROPNOTES -
preparation to identify workarounds to non critical problems#

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 úú³ xú| xú| x##□±□±±#q#"ú|ö]ú³öró|ö]ú|öró³öró³ö]ú|ö]ú|ö]#q#"ú| □ú³
 xú| □ú| xú³ xú³ □ú| □ú| □##□²□²□²#q#"ú|öró³ö□ú|öró|
ö□ú³ö□ú³öró|öró|ö#q#"ú| yú³ □ú| yú| □ú³ □ú³ yú| yú| y##□³□³□
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ú.#q#"ú|#Ŗú³#Ŗú|#Ŗú|#Ŗú³#Ŗú³#Ŗú|#Ŗú|#Ŗ##□ß□ß□ß#q#"ú|÷øDú³÷øZú|÷øDú|÷øZú³÷øZú³÷øDú|÷øDú|
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ûá#q#"ú|##ú³# ú|##ú|# ú³# ú³##ú|##ú|####ǺóǺóǺó#q#"ú|ûöú³ü

ú|ûöú|ü

ú³ü

ú³ûöú|ûöú|ûö#q#"ú|#õú³##ú|#õú|##ú³##ú³#õú|#õú|#õ##ÆôÆôÆô#q#"ú|ü

ú³ü"ú|ü

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ú|ü

ú|ü

#q#"ú|#ßú³#ðú|#ßú|#ðú³#ðú³#ßú|#ßú|#ß##ÇðÇðÇðq#"ú|ü"ú³ü7ú|ü"ú|ü7ú³ü7ú³ü"ú|ü"ú|
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